| | Application No. | Applicant(s) | |
|--|--|---|--------------------------------|
| Notice of Allowability | 10/790,262 | KIM ET AL. | |
| | Examiner | Art Unit | |
| | Huan Hoang | 2827 | |
| The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313 | (OR REMAINS) CLOSED in to or other appropriate communication is suited. | his application. If not incluication will be mailed in du | ided le course. THIS |
| 1. This communication is responsive to | | | |
| 2. The allowed claim(s) is/are 1-19. | | | |
| 3. \boxtimes The drawings filed on <u>01 March 2004</u> are accepted by the | Examiner. | | |
| 4. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: | e been received. e been received in Application | No | cation from the |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | | reply complying with the r | equirements |
| 5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give | | | NOTICE OF |
| CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the processing of the proce | son's Patent Drawing Review of the solution of | n the Office action of drawings in the front (not t | he back) of |
| 7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT | | | . Note the |
| Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date | 6. ☐ Interview Sur Paper No./M 7. ☑ Examiner's A | lail Date mendment/Comment tatement of Reasons for A | llowance |

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the claims:

Claim 15, line 11, replace "cells" in "the selected memory cells" with -cell--.

Note: There is only one memory cell selected in claim 15, lines 6-7.

2. The following is an examiner's statement of reasons for allowance:

The prior art does not teach or suggest the following limitations:

an internal clock generating circuit for generating the address clock signal and the first and second data clock signals in response to an external clock signal and complementary clock signal, wherein the address clock signal and the first and second data clock signals have twice the frequency of the external clock signal in a test mode.

a first clock generating circuit comprising a first synchronous mirror delay circuit, for generating a first internal clock signal and a second internal clock signal having 0T and T/4 phases of an external clock signal, respectively, where T is a period of the external clock signal; and

a second clock generating circuit comprising a second synchronous mirror delay circuit, for generating a third internal clock signal and a fourth internal clock signal having a T/2 and 3T/4 phases of an external clock signal, respectively;

wherein, in a test mode, the address clock signal is generated in synchronization with the first, second, third and fourth internal clock signals, the first data clock signal is generated in synchronization with the first, second and third clock signals, and the second data clock signal is generated in synchronization with the second and fourth internal clock signals.

Therefore, the application is condition for allowance.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Huan Hoang whose telephone number is (571) 272-1779. The examiner can normally be reached on Mon-Fri 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on (571) 272-1777. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

> Huan Hoang Primary Examiner Art Unit 2827

HH 3/20/05.